TMC2068 KIT

Decoder Evaluation Board Kit

Features

- Accepts analog composite or analog YC (through coaxial or S-video cables)
- Outputs analog component (YPbPr) or analog VGA
- Provides a complete 10-bit digital decoding system
- R-bus serial interface compatibility
- Raydemo software compatibility

Applications

- Evaluation of TMC22x5y 10-bit Digital Decoder
- Evaluation of TMC22071A Genlocking Video Digitizer
- Evaluation of TMC1185 10-bit ADC
- Evaluation of TMC2242 Decimation Filters
- Evaluation of TMC3003 10-bit DAC
- Compatible with Genesis 10-bit Line Doubler board
- System breadboarding

Description

The TMC2068P7C Evaluation Board Kit is a complete video 10-bit digital decoding system. The kit is composed of four Raytheon Demonstration Boards: the TMC2067P7C, TMC2068P7C, TMC2069P7C, and TMC2070P7C. The kit accepts analog composite or YC (through either BNCs or an S-video connector).

After the TMC2067P7C ADCs digitize the incoming data, it is sent to the TMC2068P7C. The TMC2068P7C accepts the digitized composite or YC data and outputs digital RGB, YCbCr, or D1. The TMC2068P7C output is then sent to the TMC2069P7C triple DAC board for conversion to analog RGB or YPbPr. VGA output is an option with the TMC2068P7C output connected to a Genesis 10-bit line-doubler and the line doubler's output connected to a TMC2069P7CG.

Although designed to showcase the TMC22x5y, the kit allows for the evaluation of several other Raytheon products: the TMC1185, TMC2242, TMC22071A, and TMC3003. For more information about Raytheon products in the kit, as well as other Raytheon products, please consult the Raytheon Semiconductor Data Book or look on the Raytheon Semiconductor website (www.raytheonsemi.com).

The TMC2067P7C, TMC2068P7C, and TMC2069P7C plug into each other as shown below in Figure 1. The TMC2070P7C is a parallel-port to R-Bus conversion board. It plugs into a PC and is linked to the kit by means of an included R-bus cable. The included Raydemo software provides a quick, simple way of altering device register maps in the TMC22x5y, TMC22071A, and onboard FPGA. This on-the-fly reconfiguration scheme permits complete evaluation of the TMC22x5y in virtually every mode of operation.

Preliminary Information

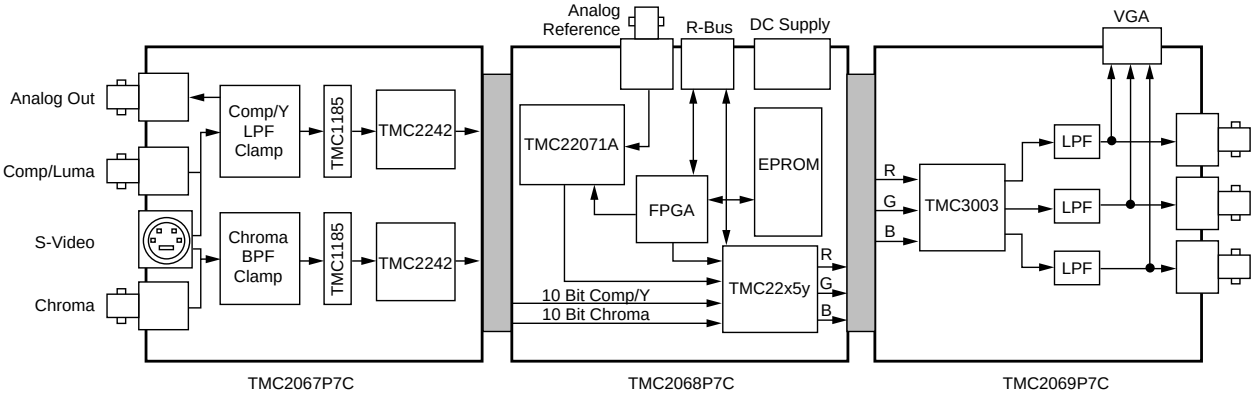

Figure 1. TMC2068P7C Kit Assembly Diagram

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Preliminary Information

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